

#### **Disclaimer and Dedication**

6,434,733 — Suryanarayana Duggirala, San Jose; Rohit Kapur, Cupertino, both of CA (US); Thomas W. Williams, Boulder, CO (US). SYSTEM AND METHOD FOR HIGH-LEVEL TEST PLANNING FOR LAYOUT. Patent dated Aug 13, 2002. Disclaimer and dedication filed Sep. 22, 2006, by the assignee, Synopsys, Inc.

Hereby disclaims and dedicates to the Public all claims of said patent.

*(Official Gazette November 7, 2006)*